

Docket No.: 50006-111

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

International Preliminary Application of

Yoshikado SANEMITSU

Serial No.: 09/902,588

Group Art Unit: 2876

Filed: July 12, 2001

Examiner: Uyen-Chau N. Le

For: PC ADAPTER CARDS AND METHOD OF MANUFACTURING THE SAME

AMENDMENT

Commissioner for Patents
Washington, DC 20231

Sir:

In response to the December 5, 2001 Office Action, please consider the following amendments and remarks.

IN THE CLAIMS:

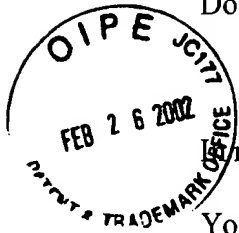
Please amend claims 1, 4 and 5 as follows, a marked-up version of the claims is appended hereto.

1. (Amended) A method of manufacturing a mini-card with a semiconductor memory device comprising the steps of:

providing an array of substrates including a plurality of individual substrates connected together;

mounting a semiconductor memory device on each of the individual substrates;

covering the individual substrates with respective cases; and



#5/A
H. N. Le
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2/28/02
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